STATUS OF THE CLAIMS

The status of the claims of the present application stands as follows:

1. **(Original)** A voltage divider circuit coupled to a power source having a source voltage and a ground, comprising:

   a first transistor including a gate electrode having an area, a source, and a drain; and

   a second transistor including a gate electrode having an area, a source, and a drain, wherein said second transistor gate electrode is joined with said first transistor source and said first transistor drain;

   wherein said first transistor gate electrode area and said second transistor gate electrode area differ by a margin greater than a typical manufacturing tolerance for transistor gate electrode areas.

2. **(Original)** A voltage divider circuit according to claim 1, wherein the power source is joined with said first transistor gate electrode.

3. **(Original)** A voltage divider circuit according to claim 1, wherein said first and second transistor gate electrode areas are selected to provide a desired division of the source voltage.

4. **(Original)** A voltage divider circuit according to claim 1, wherein said first transistor and said second transistor are arranged in series.

5. **(Original)** A voltage divider circuit according to claim 1, wherein said second transistor source and drain are joined with the ground.

6. **(Original)** A voltage divider circuit according to claim 1, wherein the voltage divider circuit does not include any resistors.

7. **(Original)** A voltage divider circuit coupled to a power source having a source voltage and a ground, comprising:

   a first transistor including a gate electrode having an area, a source, and a drain; and

a second transistor including a gate electrode having an area, a source, and a drain, wherein said second transistor gate electrode is joined with said first transistor source and said first transistor drain;

wherein said first transistor gate electrode area and said second transistor gate electrode area differ by a margin greater than a typical manufacturing tolerance for transistor gate electrode areas, said first and second transistor gate electrode areas being selected to provide a desired division of the source voltage.

8. **(Original)** A voltage divider circuit according to claim 7, wherein the power source is joined with said first transistor gate electrode.

9. **(Original)** A voltage divider circuit according to claim 7, wherein said first transistor and said second transistor are arranged in series.

10. **(Original)** A voltage divider circuit according to claim 7, wherein said second transistor source and drain are joined with the ground.

11. **(Original)** A voltage divider circuit according to claim 7, wherein the voltage divider circuit does not include any resistors.

12. **(Original)** A method of dividing a power source voltage comprising the steps of:

providing a voltage divider circuit having a first transistor including a first transistor gate electrode having a first transistor gate electrode area and a second transistor including a second transistor gate electrode having a second transistor gate electrode area;

applying the power source voltage to said voltage divider circuit; and

dividing the power source voltage according to the ratio of said first transistor gate electrode area to said second transistor gate electrode area.

13. **(Original)** A method according to claim 12, wherein said first transistor further comprises a drain and a source, each connected to said second transistor gate electrode.

14. **(Original)** A method according to claim 12, wherein said power source voltage is joined with said first transistor gate electrode.

15. **(Original)** A method according to claim 12, wherein said first transistor and said second transistor are arranged in series.

16. **(Original)** A voltage divider circuit coupled to a power source having a power source voltage and a ground, comprising:

a plurality of transistors, each including a gate electrode having a gate electrode area, a source, and a drain;

wherein said source and drain of each of said plurality of transistors is joined with said gate electrode of another of said plurality of transistors, and said gate electrode areas are equal.

17. **(Original)** A voltage divider circuit according to claim 16, wherein said plurality of transistors are arranged in series.

18. **(Original)** A voltage divider circuit according to claim 17, wherein said plurality of transistors arranged in a series includes a first transistor having a first transistor gate electrode joined with the power source.

19. **(Original)** A voltage divider circuit according to claim 16, wherein said plurality of transistors arranged in a series includes a last transistor having a last source and drain joined with the ground.

20. **(Original)** A method of designing a circuit for dividing voltage, comprising the steps of:

providing a first transistor including a gate electrode having an area, a source, and a drain; and

providing a second transistor including a gate electrode having an area, a source, and a drain;

selecting said first transistor gate electrode area and said second transistor gate electrode area according to a predetermined ratio between the areas to provide a desired voltage division; and

joining said second transistor gate electrode with said first transistor source and said first transistor drain.

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